

## ARTIFACT SHEET

Indicate quantity of item(s) received by the USPTO but not scanned.

- ☐ Priority Document No. 046724 Country Japanese  
Doc Code: FRPR
- ☐ CD(s) containing computer program listing  
Doc Code: Computer
- ☐ Stapled Set(s) of Extra Color Drawings  
Doc Code: Artifact
- ☐ CD(s) containing pages of specification ☐  
and/or sequence listing ☐  
Doc Code: Artifact
- ☐ CD(s) with content unspecified  
Doc Code: Artifact
- ☐ Microfilm(s)  
Doc Code: Artifact
- ☐ Video tape(s)  
Doc Code: Artifact
- ☐ Other mass storage media, description: \_\_\_\_\_  
Doc Code: Artifact
- ☐ Model(s)  
Doc Code: Artifact
- ☐ Other, description: \_\_\_\_\_  
Doc Code: Artifact
- ☐ Other, description: \_\_\_\_\_  
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Doc Code: Artifact

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PRIORITY  
4-9-01  
ACRES

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yoshinori MIYAKI, et al  
Serial No.:  
Filed: February 20, 2001  
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND OF MANUFACTURING THE SAME  
Group:



LETTER CLAIMING RIGHT OF PRIORITY

Honorable Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

February 20, 2001

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55, the applicant(s) hereby claim(s) the right of priority based on Japanese Patent Application No.(s) 2000-046724 filed February 18, 2000.

A certified copy of said Japanese Application is attached.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

A handwritten signature in black ink, appearing to read "Gregory E. Montone".

Gregory E. Montone  
Registration No. 28,41

GEM/nac  
Attachment  
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